

L Number	Hits	Search Text	DB	Time stamp
1	658	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:38
3	85	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and (714/.ccls.)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:46
4	108	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and (scan adj3 test\$3)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:45
5	124	(clock adj3 domain\$1) and (scan adj3 test\$3)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:40
6	86	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and (scan adj3 test\$3) and (MUX or multiplex\$3)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:41
7	96	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and ((scan adj3 test\$3) or (scan adj2 chain\$2)) and (MUX or multiplex\$3)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:52
8	10	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and (714/724.ccls.)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:46
9	0	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and (714/725.ccls.)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:46
10	20	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and (714/726.ccls.)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:46
11	8	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and (714/729.ccls.)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:46
12	25	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) same (clock adj3 domain\$1) and (714/.ccls.)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:47
13	96	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and ((scan adj3 test\$3) or (scan adj2 chain\$2)) and (MUX or multiplex\$3) and logic	USPAT; EPO; JPO; DERWENT	2004/03/11 10:07
14	5	((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1) and ((scan adj3 test\$3) or (scan adj2 chain\$2)) and (MUX or multiplex\$3) and (combinatorial near3 logic)	USPAT; EPO; JPO; DERWENT	2004/03/11 09:54
15	16	(first adj2 clock adj2 domain) and (second adj2 clock adj2 domain) and (third adj2 clock adj2 domain)	USPAT; EPO; JPO; DERWENT	2004/03/11 10:32
16	1	(first adj2 clock adj2 domain) and (second adj2 clock adj2 domain) and (third adj2 clock adj2 domain) and (714/.ccls.)	USPAT; EPO; JPO; DERWENT	2004/03/11 10:32
17	12	(first adj2 clock adj2 domain) and (second adj2 clock adj2 domain) and (third adj2 clock adj2 domain) and ((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1))	USPAT; EPO; JPO; DERWENT	2004/03/11 10:32
18	113	(first adj2 clock adj2 domain) and (second adj2 clock adj2 domain) and ((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1))	USPAT; EPO; JPO; DERWENT	2004/03/11 10:33
22	2	(first adj2 clock adj2 domain) and (second adj2 clock adj2 domain) and ((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1)) and (multiplex\$3 or MUX) and (combinatorial adj2 logic)	USPAT; EPO; JPO; DERWENT	2004/03/11 10:34

21	2	(first adj2 clock adj2 domain) and (second adj2 clock adj2 domain) and (((edge adj2 triggered) or (flip-flop\$1) or latch\$2) and (clock adj3 domain\$1)) and (multiplex\$3 or MUX) and (combinatorial adj2 logic)	USPAT; EPO; JPO; DERWENT	2004/03/11 10:34
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